IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Jason Wei		
Assignee:	Rambus Inc.		
Title:	"System and Method for Selecting Optimal Data Transition Types for Clock and Data Recovery"		
Serial No.:	Unknown	Filed:	March 9, 2004
Examiner:	Unknown	Tel:	Unknown
Docket No.:	RA327.P.US	Art Unit:	Unknown

Mail Stop Patent Application
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 9, 2004

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, §1.97 and §1.98, Applicant brings the 3 documents listed on the enclosed form PTO-1449 to the Examiner's attention in the above-captioned application. Citation of the listed documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant application;
2. a representation that a search has been made; or
3. an admission that the information cited is, or is considered to be, material to patentability as defined in §1.56(b).

I hereby certify that this correspondence is being deposited with the United States Postal Service as "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated below and is addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

March 9, 2004
Date

Laurie Moreno

Express Mail No.: ER 265640975 US

Respectfully submitted,

Arthur J. Behiel
Attorney for Applicants
Reg. No. 39,603

U.S. Department of Commerce, Patent and Trademark Office	Serial No.: Unknown
	Filing Date: March 9, 2004
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventor: Jason Wei
"SYSTEM AND METHOD FOR SELECTING OPTIMAL DATA TRANSITION TYPES FOR CLOCK AND DATA RECOVERY"	Group Art Unit: Unknown
	Examiner Name: Unknown
Express Mail Receipt No. ER 265640975 US	Attorney Docket No.: RA327.P.US

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A						
	B						
	C						
	D						
	E						

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	F							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	G	Stojanovi, Vladimir et al. "Adaptive Equalization and Data Recovery in a Dual-Mode (PAM2/4) Serial Link Transceiver." Rambus, Inc. Department of Electrical Engineering, Stanford University. January 2004. 4 pages.
	H	Zerbe, J. et al. "Equalization and Clock Recovery for a 2.5 - 10Gbs 2-PAM/4-PAM Backplane Transceiver Cell." Presented at ISSCC 2003, paper 4.6. 2 pages.
	I	Zerbe, J. et al. "Equalization and Clock Recovery for a 2.5-10-Gb/s 2-PAM/4-PAM Backplane Transceiver Cell." IEEE Journal of Solid-State Circuits, Vol.38, No.12, December 2003. Pages 2121-2130.
	J	
	K	

Examiner	Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.